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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of: **Takatoshi DEGUCHI et al.**

Group Art Unit: **2813**

Application Number: **10/695,666**

Examiner: **Nema O. Berezny**

Filed: **October 29, 2003**

Confirmation Number: **4071**

For: **METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE
HAVING A MULTILAYER INTERCONNECTION STRUCTURE**

Attorney Docket Number: **032071**

Customer Number: **38834**

RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCESSING REQUESTED

MAILSTOP: AF

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Date: July 26, 2006

Sir:

This paper is filed in response to the Office Action dated April 27, 2005.

Remarks begin on page 2 of this paper.